

FIG. 1

100

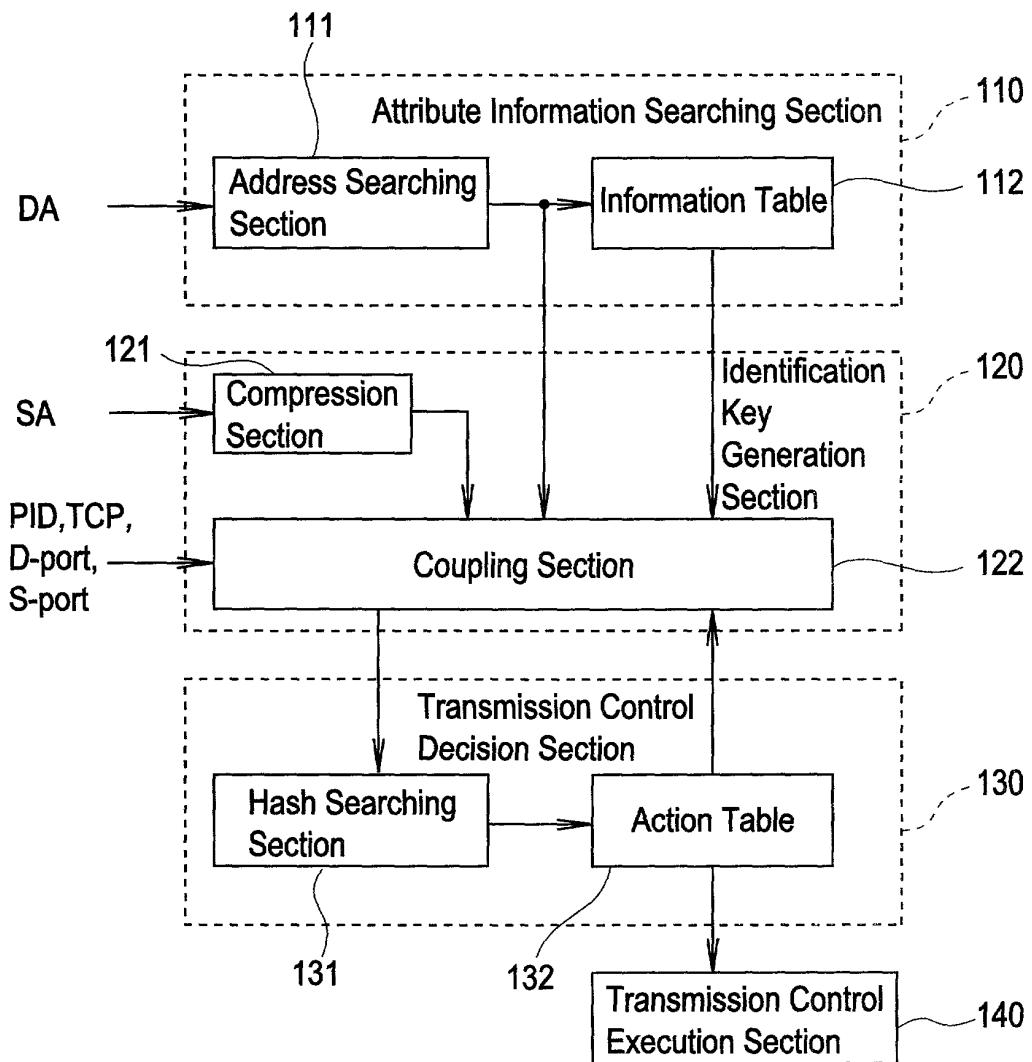


FIG.2A

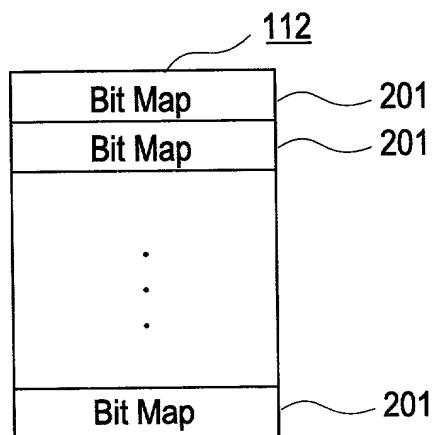
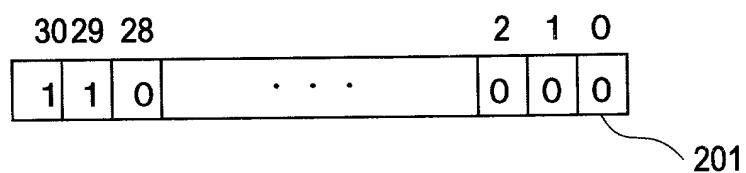


FIG.2B



10000000000000000000000000000000

FIG.3

D-port (16 Bits)	S-port (16 Bits)	TCP (8 Bits)	PID (8 Bits)	SA (18 Bits)	Index Value (18 Bits)
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FIG.4

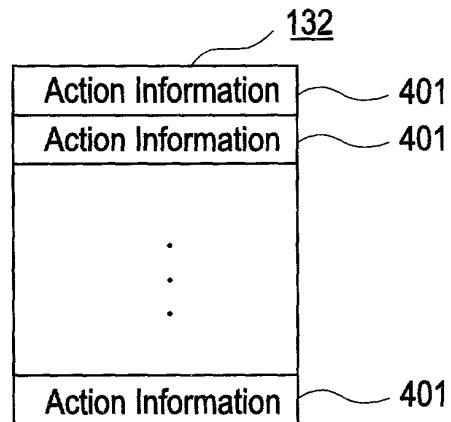


FIG.5

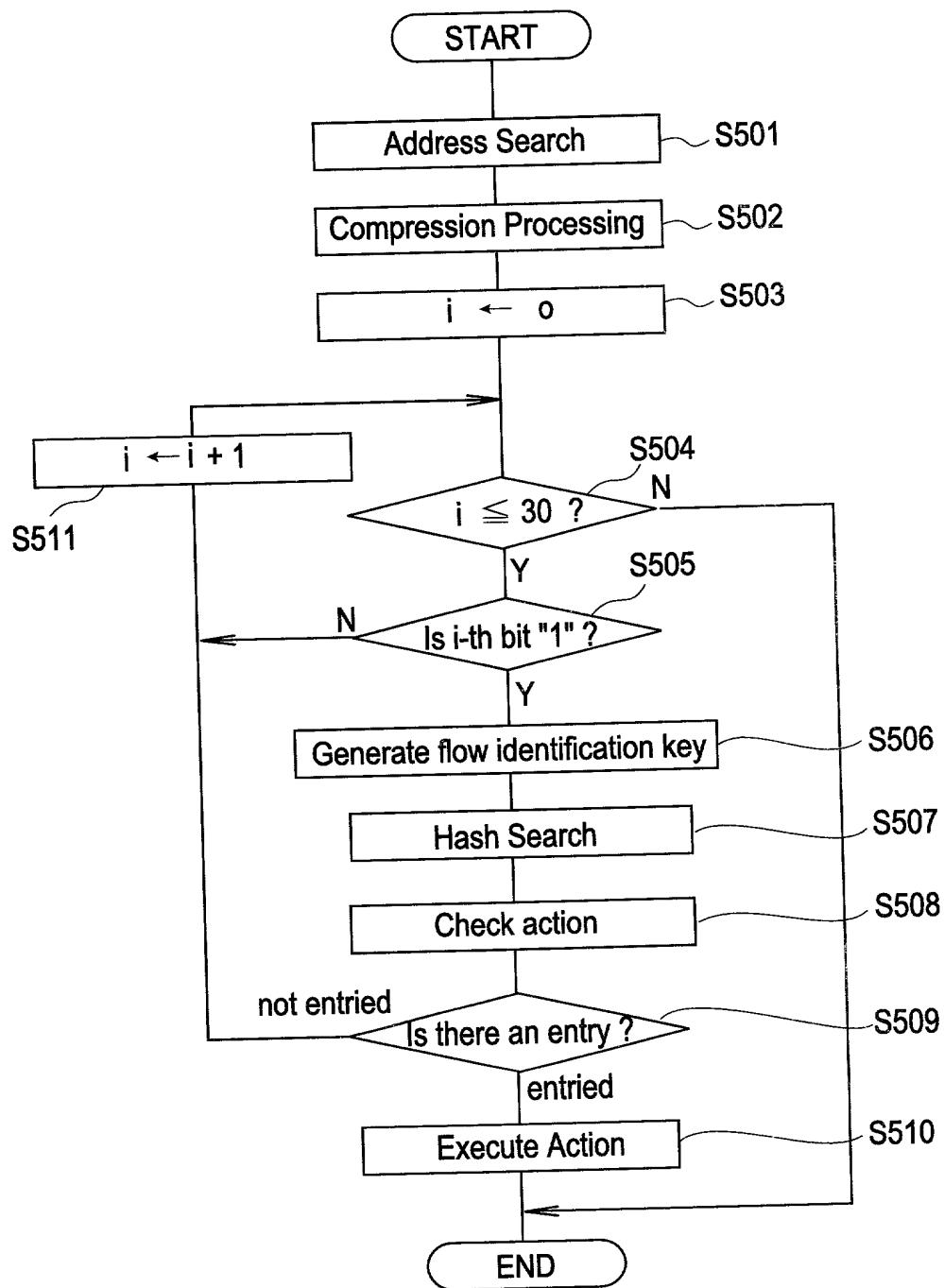


FIG. 6

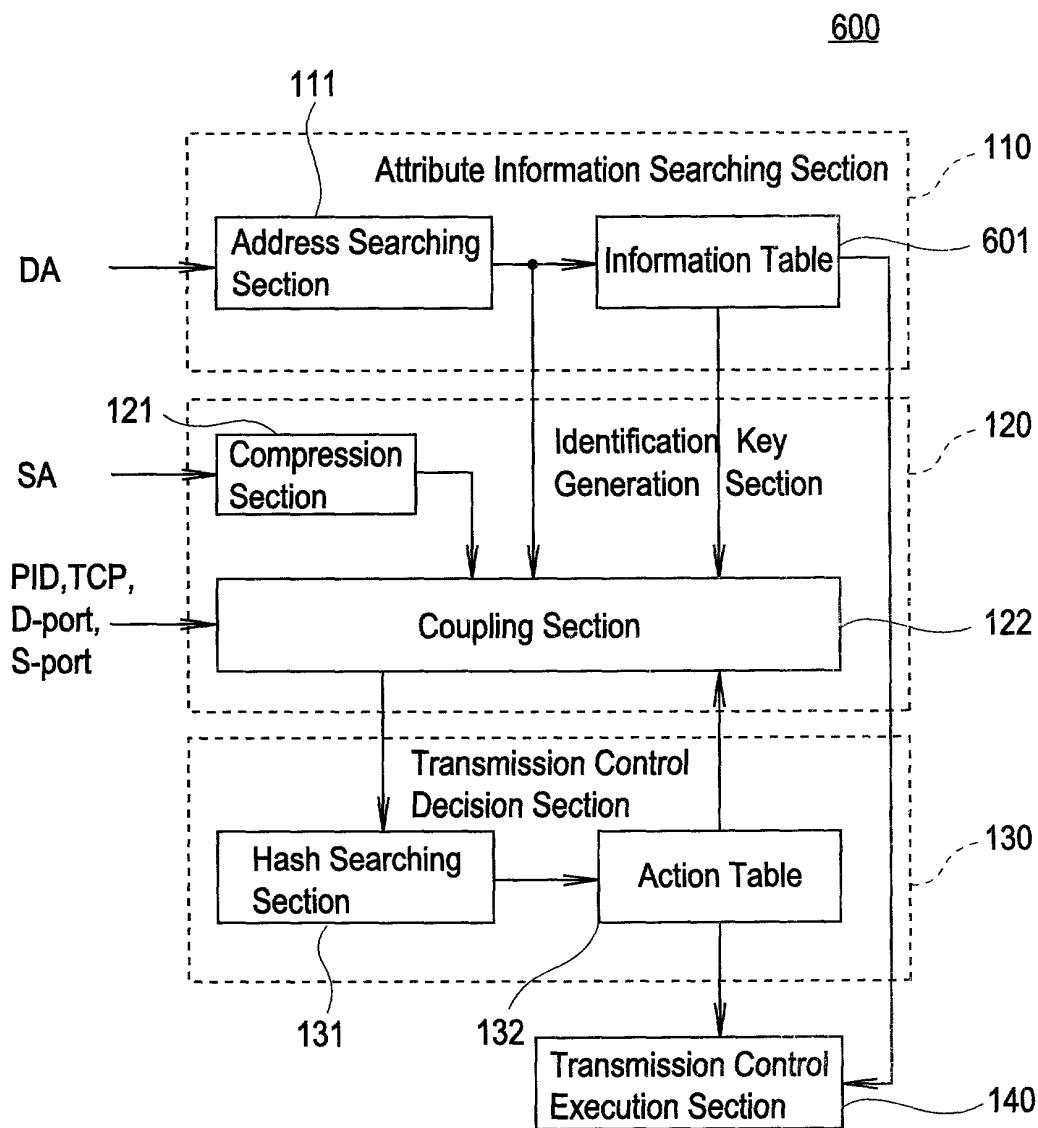


FIG. 7

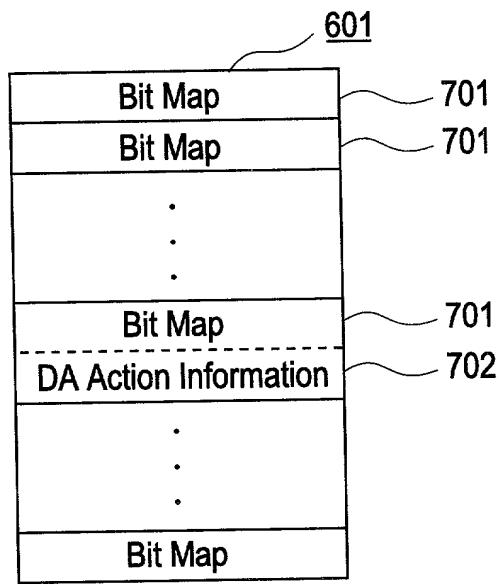
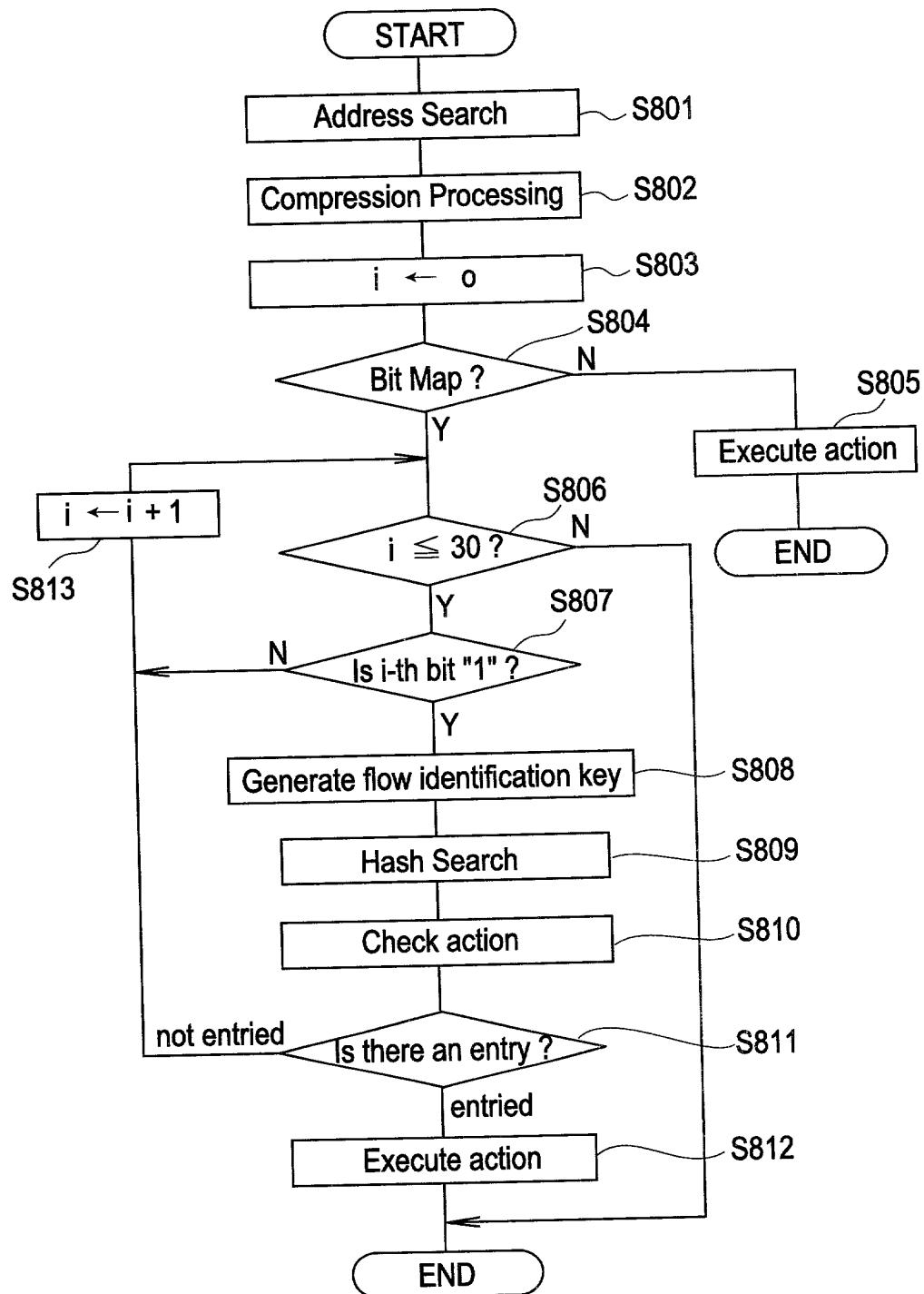


FIG.8



10005818 20200204

FIG.9

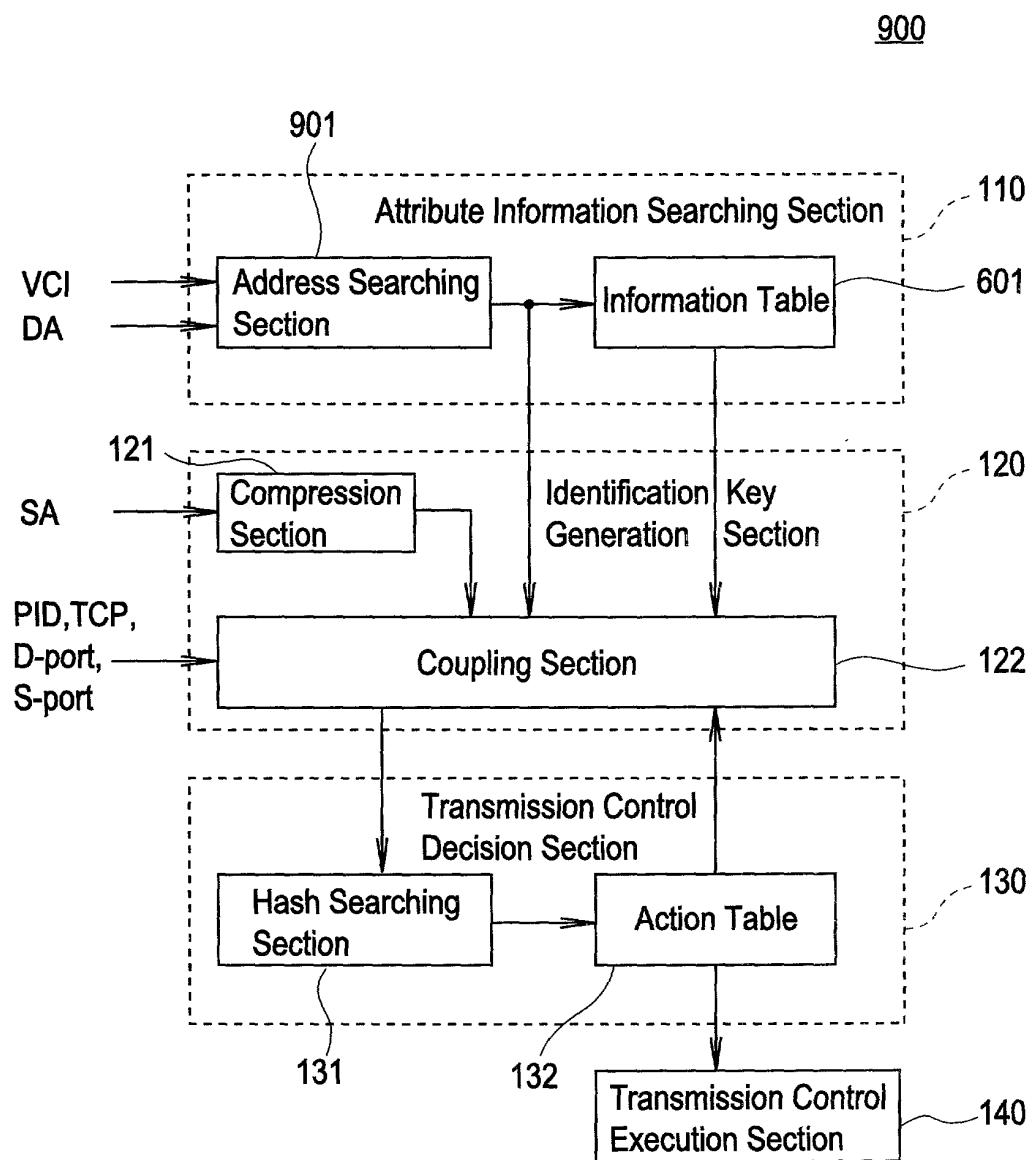


FIG. 10

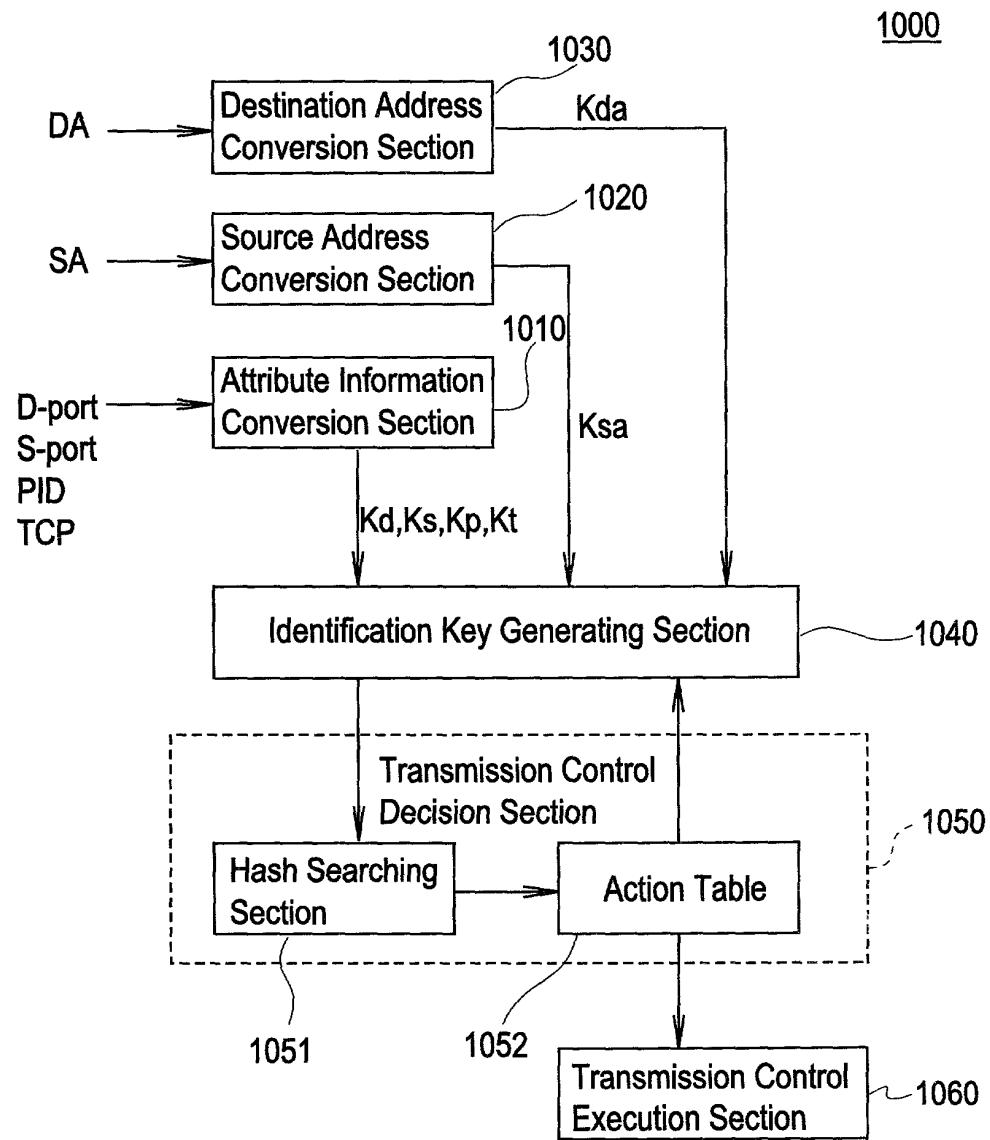


FIG. 11

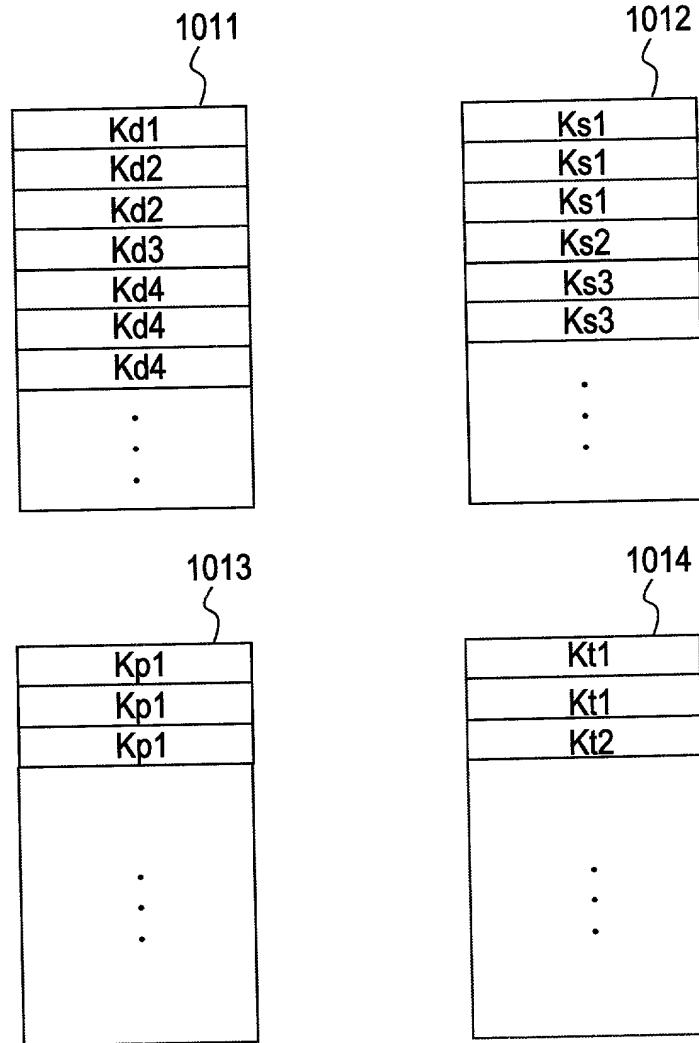


FIG. 12

Kd (16 Bits)	Ks (16 Bits)	Kt (8 Bits)	Kp (8 Bits)	Ksa (18 Bits)	Kda (18 Bits)
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FIG. 13

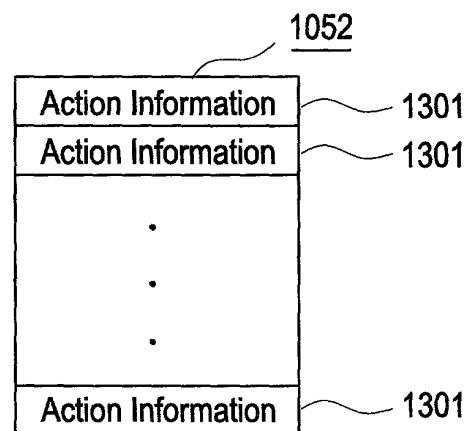


FIG. 14A

Rule	D-port	S-port	PID,TCP,DA,SA	Action
1	'00000000000000000001' '00000000000000000011'	'00000000000000000001' '000000000000000000100'	×	A1
2	'00000000000000000010' '000000000000000000100'	'000000000000000000101' '000000000000000000110'	×	A2
3	'000000000000000000101' '000000000000000000111'	'00000000000000000001' '00000000000000000011'	×	A3

FIG. 14B

D-port	Kd	S-port	Ks
'00000000000000000001'	Kd1	'00000000000000000001' '00000000000000000011'	Ks1
'00000000000000000010' '00000000000000000011'	Kd2	'000000000000000000100'	Ks2
'000000000000000000100'	Kd3	'000000000000000000101' '000000000000000000110'	Ks3
'000000000000000000101' '000000000000000000111'	Kd4		

FIG. 14C

Kd	Ks	Action
Kd1	Ks1	A1
Kd1	Ks2	A1
Kd2	Ks1	A1
Kd2	Ks2	A1
Kd2	Ks3	A2
Kd3	Kd3	A2
Kd4	Ks1	A3

FIG. 15

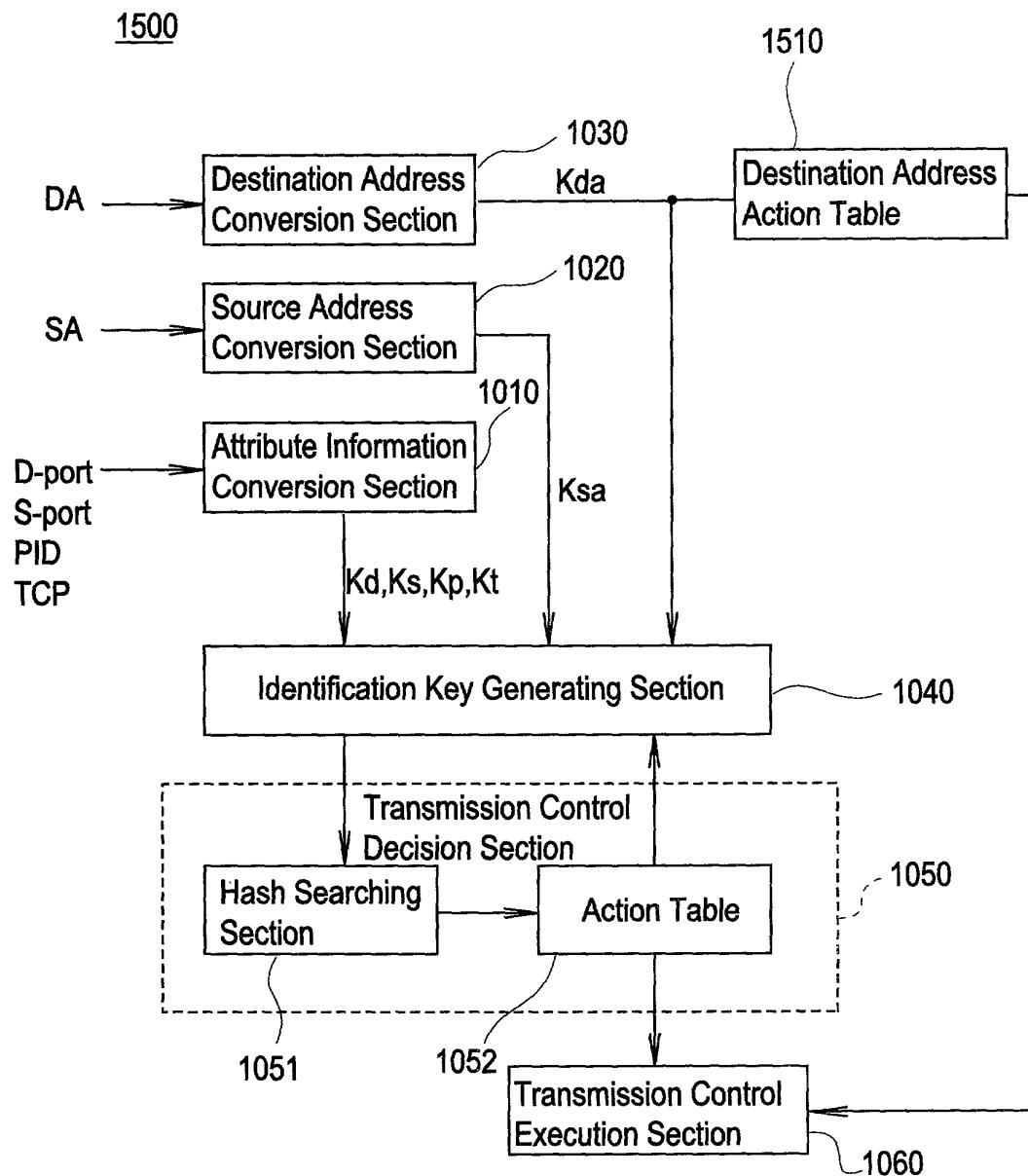


FIG. 16

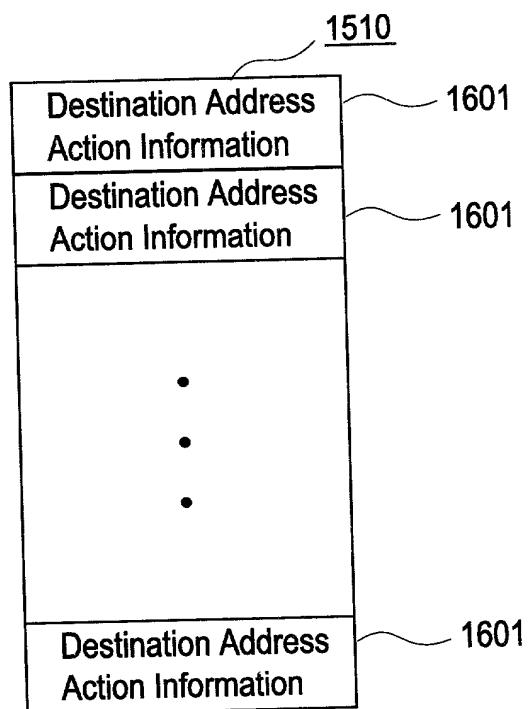


FIG. 17

